

	Application No.	Applicant(s)	
Notice of Allowability	10/629,232	LEE ET AL.	
	Examiner	Art Unit	
	Ingrid Wright	2835	
The MAILING DATE of this communication appears on the cover sheet with the correspondence address All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS. This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.			
1. This communication is responsive to <u>4/30/07</u> .			
2. The allowed claim(s) is/are <u>1-6,11-15,17-21</u> .			
3.			
 Attachment(s) 1. ☑ Notice of References Cited (PTO-892) 2. ☐ Notice of Draftperson's Patent Drawing Review (PTO-948) 3. ☐ Information Disclosure Statements (PTO/SB/08), Paper No./Mail Date 4. ☐ Examiner's Comment Regarding Requirement for Deposit of Biological Material 	5. ☐ Notice of Informal P 6. ☐ Interview Summary Paper No./Mail Da 7. ☐ Examiner's Amendr 8. ☑ Examiner's Stateme 9. ☐ Other	(PTO-413), te	•

DETAILED ACTION

Allowable Subject Matter

- 1. Claims 1-7-6,11-15,17-21 are allowed.
- 2. The following is an examiner's statement of reasons for allowance: the allowability resides in the overall structure of the device as recited by independent claims 1 & 12, and at least in part, because claim 1 recites: "the retention module defining two positioning holes at symmetrically opposite sides of a center thereof, two pins positioned in the positioning holes and welded to the printed circuit board," and claim 12 recites: "two pins extending from the retention module to the printed circuit board and welded to the printed circuit board." The aforementioned limitations in combination with all the remaining limitations of claims 1 & 12, are believed to render the claims 1 & 12, and all claims dependent therefrom, patentable over the art of record.

US 6639800 B1 to Eyman et al. is considered a close reference to the instant application. Eyman et al. disclosed a printed circuit board (204), positioning holes (e.g. holes near area around (31A,31B), a retention module (30) and screws (90), but is silent as to two pins positioned in the positioning holes and welded to the printed circuit board (204).

US 6741470 B2 to Isenburg is considered a close reference to the instant application. Isenburg disclosed a printed circuit board (102), positioning holes (115), and a retention module (108), and portions of pins (105) being welded (col. 8, lines 1-5 of Eyman et al.), but is silent as the two pins (105) positioned in the positioning holes (115) and welded to the printed circuit board (102).

Note: US 6549412 to Ma has the same assignee as the instant application.

US 6549412 B1 to Ma is considered the closest reference to the instant application. Ma disclosed a printed circuit board (20), a retention module (10), and positioning holes (14), and pins (32). Although, Ma teaches pins (32) affixed to the printed circuit board (20), a rejection under statute U.S.C. 103 is not permitted, as this reference has the same assignee as the instant application.

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The Examiner has cited the following pertinent references: US 5734556, US 6209623 B1, US 5384940, US 6809929 B2, US 5870286, & US 6778396 B2 disclosed heat sink mounting assemblies.

None of the cited reference, either alone or in combination, is believed to render the present invention unpatentable as claimed.

- 3. Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."
- 4. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Ingrid Wright whose telephone number is (571)272-8392. The examiner can normally be reached on M-F. If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Jayprakash Gandhi can be reached on (571)272-2800, ext 35. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pairdirect.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

IDW